

ABSTRACT

The present invention provides an apparatus, system and method for
5 synchronizing a local clock signal with a remote clock signal in a communication
network. Phase information is used to calculate a number of “clock jitters” per
unit of time needed to synchronize the locally generated clock with the remote
clock. Introducing (removing) a given amount of delay at a particular point in the
local clock signal results in a positive (negative) jitter in which its minimum value
10 defines the jitter resolution. The jitters are introduced to the local clock signal
from a plurality of tapped delay line elements (310) selected by a phase selector
(350) in response to a timing correction signal issued by a phase error module
(520).